

## WEST Search History

Hide Items Restore Clear Cancel

DATE: Wednesday, June 13, 2007

Hide?	Set Name	Query	Hit Count
<input type="checkbox"/>	DB=PGPB,USPT,USOC;	THES=ASSIGNEE; PLUR=YES; OP=ADJ	
<input type="checkbox"/>	L50	L46 and (PWM or (pulse width modulation))	2
<input type="checkbox"/>	L49	L45 and (PWM or (pulse width modulation))	8
<input type="checkbox"/>	L48	((frame frequency) same noise) and L18	8
<input type="checkbox"/>	L47	L46 and @pd > 20070613	0
<input type="checkbox"/>	L46	L15 same ((frame frequency) same noise)	7
<input type="checkbox"/>	L45	L15 and ((frame frequency) same noise)	30
<input type="checkbox"/>	L44	L42 not @AY>2004	57
<input type="checkbox"/>	L43	(noise) and L19	14
<input type="checkbox"/>	L42	(noise) and L18	63
<input type="checkbox"/>	L41	(electronic interference) and L19	1
<input type="checkbox"/>	L40	(electronic interference) and L18	1
<input type="checkbox"/>	L39	(noise) and L5	12
<input type="checkbox"/>	L38	(noise) and L3	30
<input type="checkbox"/>	L37	(electronic interference) and L5	0
<input type="checkbox"/>	L36	(electronic interference) and L3	0
<input type="checkbox"/>	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD;	THES=ASSIGNEE; PLUR=YES; OP=ADJ	
<input type="checkbox"/>	L35	(PWM).clm.) and L31	34
<input type="checkbox"/>	L34	(PWM).clm.) and L30	5
<input type="checkbox"/>	L33	((electronic interference).clm.) and L31	1
<input type="checkbox"/>	L32	((electronic interference).clm.) and L30	1
<input type="checkbox"/>	L31	lg-electronics\$.as.	88946
<input type="checkbox"/>	L30	woo-jong-hyun\$.in.	13
<input type="checkbox"/>	DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L29	L21 and (PWM or (pulse width modulation))	3
<input type="checkbox"/>	L28	L20 and (PWM or (pulse width modulation))	31
<input type="checkbox"/>	DB=PGPB; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L27	L21 and (PWM or (pulse width modulation))	7
<input type="checkbox"/>	L26	L20 and (PWM or (pulse width modulation))	20
<input type="checkbox"/>	DB=EPAB,JPAB,DWPI; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L25	L21 and (PWM or (pulse width modulation))	3

<input type="checkbox"/>	L24	L20 and (PWM or (pulse width modulation))	3
<input type="checkbox"/>	DB=PGPB,USPT,EPAB,JPAB,DWPI; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L23	L21 and (PWM or (pulse width modulation))	13
<input type="checkbox"/>	L22	L20 and (PWM or (pulse width modulation))	54
<input type="checkbox"/>	L21	L17 and interference	96
<input type="checkbox"/>	L20	L16 and interference	256
<input type="checkbox"/>	L19	L17 and (PWM or (pulse width modulation))	65
<input type="checkbox"/>	L18	L16 and (PWM or (pulse width modulation))	203
<input type="checkbox"/>	L17	L15 same (frame frequency)	774
<input type="checkbox"/>	L16	L15 and (frame frequency)	1935
<input type="checkbox"/>	L15	(liquid crystal display) or LCD	485240
<input type="checkbox"/>	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L14	L13 and (frame frequency)	10
<input type="checkbox"/>	L13	L11 and L12	397
<input type="checkbox"/>	L12	inverter same (PWM or (pulse width modulation))	6695
<input type="checkbox"/>	L11	liquid crystal display	166614
<input type="checkbox"/>	DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L10	L3 not L5	43
<input type="checkbox"/>	L9	L4 and (PWM or (pulse width modulation))	37
<input type="checkbox"/>	DB=PGPB; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L8	L3 not L5	48
<input type="checkbox"/>	L7	L4 and (PWM or (pulse width modulation))	2
<input type="checkbox"/>	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L6	L3 not L5	91
<input type="checkbox"/>	L5	L4 and (PWM or (pulse width modulation))	39
<input type="checkbox"/>	L4	(345/213)[CCLS]	704
<input type="checkbox"/>	L3	L2 and inverter	103
<input type="checkbox"/>	L2	L1 and (PWM or (pulse width modulation))	372
<input type="checkbox"/>	L1	(345/204 [345/211 [345/212 [345/213])[CCLS]	4776

END OF SEARCH HISTORY